CLAIMS

1.　　　　A data processing system in which a non-volatile memory apparatus having a plurality of storages in which one cluster is constructed by a

5　plurality of sectors and one segment is constructed by a plurality of clusters is detachable to/from a data processing apparatus, wherein

said data processing apparatus has

address designating means for designating

10　an address of the cluster in which data is recorded,

said memory apparatus has

recording means for recording the data into the address designated by said address designating means, and

15　　　　the data of said plurality of clusters in said one segment is distributed and arranged into said plurality of storages.

2.　　　　A data processing apparatus using a non-volatile memory apparatus, as a recording medium,

20　having a plurality of storages in which one cluster is constructed by a plurality of sectors and one segment is constructed by a plurality of clusters, wherein

said data is written into said memory

25　apparatus so that data of said plurality of clusters in said one segment is distributed and arranged into said plurality of storages.

32

3.   A non-volatile memory apparatus which is
detachable to/from a data processing apparatus and
has a plurality of storages in which one cluster is
constructed by a plurality of sectors and one
5   segment is constructed by a plurality of clusters,
wherein

data of said plurality of clusters in said
one segment is distributed and arranged into said
plurality of storages.

10   4.   An apparatus according to claim 1, 2, or 3,
wherein

an access is performed with reference to a
logic cluster address/physical cluster address
conversion table.

15   5.   A memory apparatus according to claim 3,
wherein

a signal to switch said plurality of
storages is formed from one or a plurality of bits
on the lower side of an address.

20   6.   A data recording method of recording, in
parallel, data in which one cluster is constructed
by a plurality of sectors and which exists over a
plurality of said plurality of clusters into a
plurality of storages, comprising the steps of:

25   designating a cluster address and writing
data into the designated cluster address; and

after completion of the parallel writing

33

process, distributing and arranging the data of said
plurality of clusters in one segment into said
plurality of storages.

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